

WHAT IS CLAIMED IS:

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1. A device for processing data signals,
comprising:

a storing part storing an input signal;

and

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an extracting part extracting said data
signals included in said input signal from said
storing part and outputting said data signals at a
desired output speed,

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wherein said extracting part outputs said
data signals based on storage information of said
input signal.

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2. The device as claimed in claim 1,
wherein:

said storing part includes a first memory
part, a second memory part, and a third memory part;

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said input signal is stored in an order of
the first memory part, the second memory part, and
the third memory part; and

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said second memory part signals said
extracting part of said storage information of said
second memory part.

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3. The device as claimed in claim 1,
wherein when predetermined data is notified from said
storing part, said extracting part outputs said data

signals, in which an invalid data signal is inserted,
to said input signal.

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4. The device as claimed in claim 1,
wherein when predetermined data is notified from said
storing part, said extracting part outputs said data
10 signals in which an invalid data signal is included
in said input signal.

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5. The device as claimed in claim 1,
wherein said extracting part comprises:

a monitoring part monitoring said data
signals input from said storing part;

20 a data determining part determining said
data signals based on a notice of said storage
information from said storing part and a notice of
validity of said data signals from said monitoring
part; and

25 an invalid data generating part generating
invalid data to insert into said input signal,

wherein said invalid data generating part
inserts said invalid data into said input signal in
response to a determination notice from said data
30 determining part

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6. The device as claimed in claim 1,
wherein said extracting part comprises:

a monitoring part monitoring said data

signals output from said storing part;

a no-data code determining part
determining a no-data code based on a notice of said
storage information from said storing part and a
5 notice of validity of said data signals from said
monitoring part; and

a deleting part deleting said no-data code
included in said input signal in response to a
determination notice.

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7. A device for multiplexing data signals,
15 comprising a multiplexing part multiplexing data
signals output from a plurality of signal processing
parts and outputting a multiplexed data signal,

wherein each of said plurality of signal
processing parts comprises:

20 a storing part storing an input signal;
and

an extracting part extracting data signals
included in said input signal from said storing part
and outputting said data signals at a desired output
25 speed,

wherein said extracting part outputs said
data signals based on storage information of said
input signal.

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8. A method for processing data signals
35 included in an input data signal to output at a
desired output speed, said method comprising the
steps of:

outputting said data signals in which an invalid data signal is inserted, at said desired output speed, when an input speed of said input data signal is slower than said desired output speed,

- 5 outputting said data signals in which said invalid data signal included in said data signals is deleted, at said desired output speed, when said input speed is faster than said desired output speed.

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